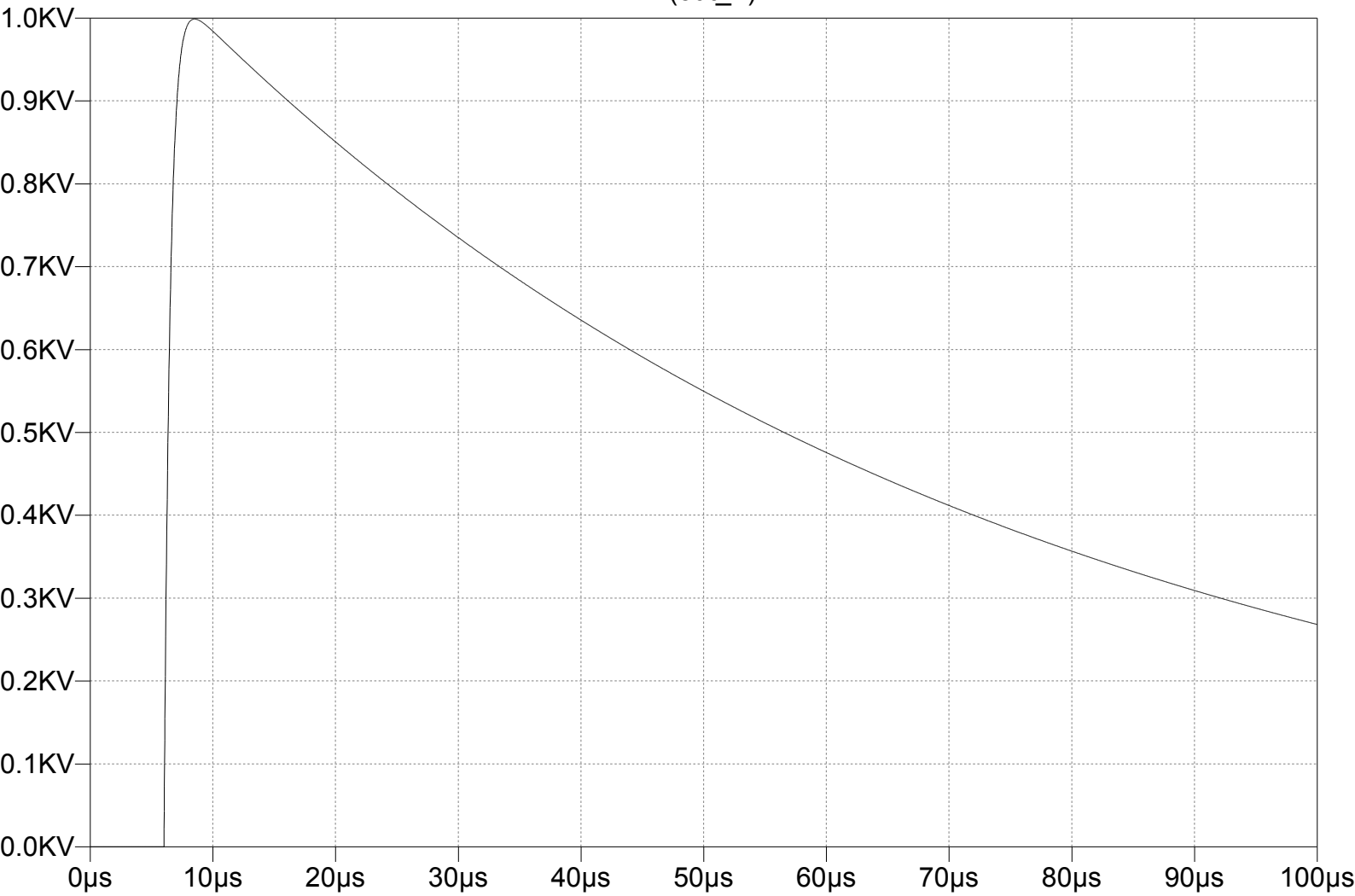
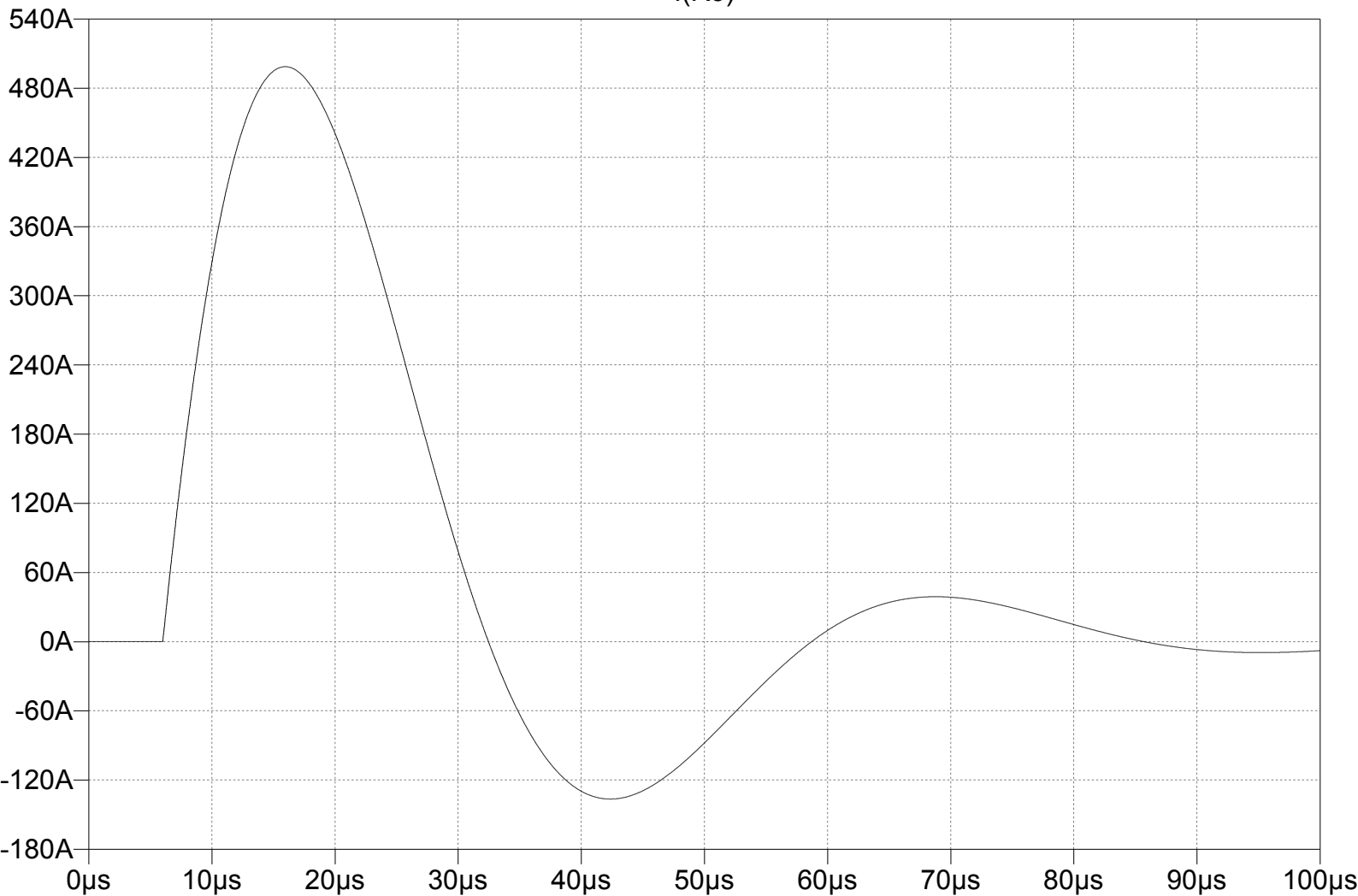


V(out\_+)



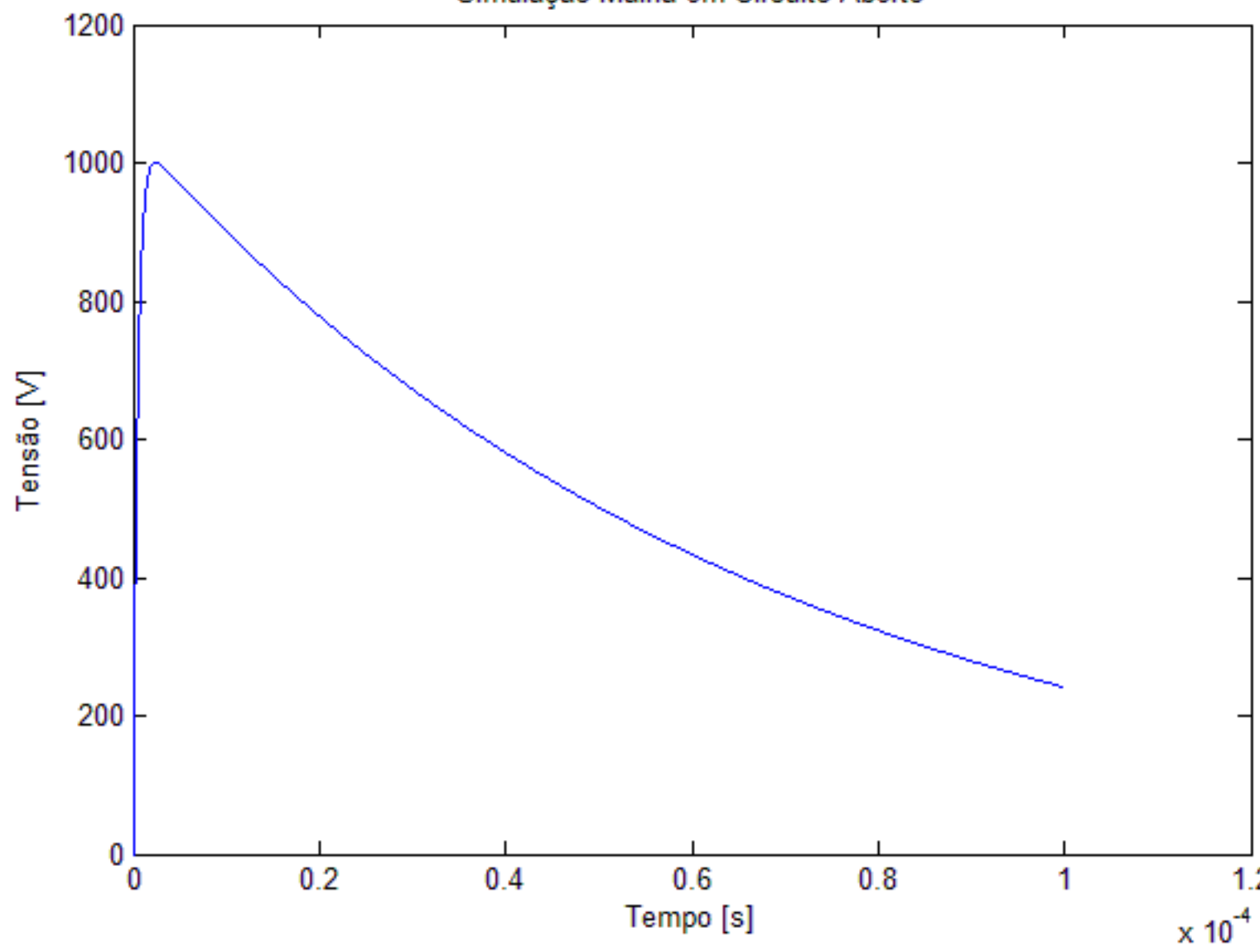
--- C:\Users\Bruno Aguiam\Desktop\TESE\Malha\_1kV\_Biblio.raw ---

I(R5)



--- C:\Users\Bruno Aguiam\Desktop\TESE\Malha\_1kV\_Biblio.raw ---

Simulação Malha em Circuito Aberto



Simulação Malha em Curto-Circuito

